※ W836x7HG CIR port driver for Vista MCE

Provided by Nuvoton

Provided by Nuvoton
(Sample ASL code)

Provided by Microsoft

6x7HG H/W

CIRClass driver
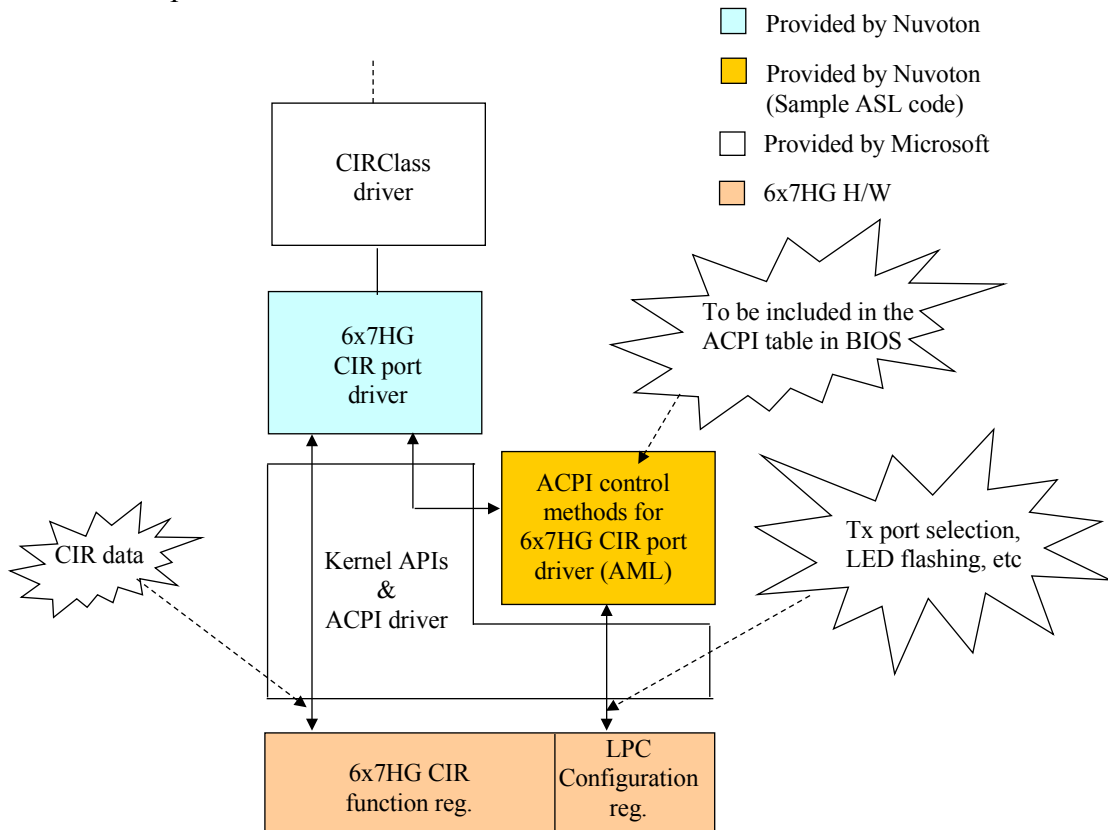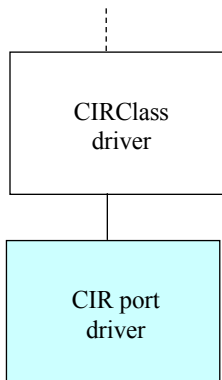
CIR port driver

Specific ACPI control methods for 6x7HG CIR port driver

Method(NCAP,0x00)
{
// Returns the capability bit masks which tell the 6x7HG CIR port driver that
// 1) if flashing the LED based on Vista MC CIR port driver requirement (NLED),
// 2) if flashing the LED when receiving IR data (NRXL),
// 3) EFER offset (2Eh/4E) for used with NRXL control method,
// 4) the number of Tx emitters, which will be reported to the CIR class driver.
}

Method(NRXL,0x00)
{
// Specifies how the 6x7HG CIR port driver powers on or off the Rx LED and
// which GPO bit is connected to the Rx LED.
// The CIR port driver will power on the Rx LED when receiving the IR data and
// power off the Rx LED if no Rx traffic.
}

Method(NJKP,0x00)
{
// Returns the Tx emitter presence status. When the Tx emitter presense status is
// requested by CIR class driver, the CIR port driver will ask the information from
// this control method and report it to the CIR class driver.
}

Method(NTXP,0x00)
{
// When CIR class driver requests to send IR data, it will also specify which Tx
// emitter will be used for the Tx data. The 6x7 CIR port driver then uses this
// control method to select the correct Tx emitter.
// This control method usually sets the configuration regsiter bits to enable or
// disable the specific Tx signal (for 677) or sets the GPO bit to control the
// extern H/W circuti to select which Tx emitter to be used (for 667).
}

Method(NLED,0x00)
{
// Flashing the LED when requested by the CIR class driver.
}